1		2	3
MOTOROLA CONFIDENTIAL PROPRIETARY			
	Pacaband UP		RF HB
	Baseband HB	DEE OCO 1E DOM	
	REF_0SC_15_36M BB_CLK_15_36M TEMP_SENSE 15_36M_CLK_EN• BBIF_RX_FRAME	REF_0SC_15_36M BB_CLK_15_36M	REF_OSC_15_36M BB_CLK_15_36M TEMP_SENSE 5_36M_CLK_EN*
	TEMP_SENSE 15_36M_CLK_EN*	TEMP_SENSE 15_36M_CLK_EN* BBIF_RX_FRAME	TEMP_SENSE 15_36M_CLK_EN*
	BBIF_RX_FRAME BBIF_TX_FRAME BBIF_CLK	BBIF_IX_FRAME BBIF_CLK	BBIF_KX_FRAME
	BBIF_ULK BBIF_TX(7:0) BBIF_RX(5:0)	BBIF_TX(7:0) BBIF_RX(5:0)	BBIF_CLK BBIF_TX(7:0) BBIF_RX(7:2)
	HL_TX_EN	HL_TX_EN	
	HARMONY CS	HARMONY_CS	HARMONY CS
	WB_SPI_MISO WB_SPI_CLK WB_SPI_MOSI	WB_SPI_MISO WB_SPI_CLK WB_SPI_MOSI	WB_SPI_MISO WB_SPI_CLK WB_SPI_MOSI
	RF_RESET*	WB_RX_ON	RF_RESET*
	WB_RX_ON WB_TX_RAMP	WB_TX_RAMP WB_RX_ACQ	WB_RX_ON WB_TX_RAMP
	WB_RX_ACQ WB_TX_SLOT	WB_TX_SLOT WB_RX_SLOT AOC_PWR_UP_DOWN	
	WB_RX_SLOT AOC_PWR_UP_DOWN WB_TX_PREKEY	ACC_PWR_UP_DOWN WB_TX_PREKEY	WB_RX_SLÖT AOC_PWR_UP_DOWN WB_TX_PREKEY
	WD_IA_I KEKEI		
	NB_RX_ACQ	NB_RX_ACO	NB_RX_ACQ
	NB_TX_KEYM	NB_TX_KEYM NB_RX_EN	NB_TX_KEYM
	NB_RX_EN	NB_TX_EN NB_TX_EN NB_EXC_EN•	
	NB_EXC_EN*	DET_FLAG	NB_EXC_EN*
	DET_FLAG TX_RX_ERROR	TX_RX_ERROR	DET_FLAG
	CLK_SEL	CLK_SEL	TX_RX_ERROR
		MAGIC_CS	
	MAGIC_CS NB_SPI_MOSI NB_SPI_CLK	NB_SPI_MOSI NB_SPI_CLK	MAGIC_CS NB_SPI_MOSI NB_SPI_CLK
	DMCS_MAGIC BDX BFSR	BDX BF SR	DMCS_MAGIC SDTX BFSR
	BCLKR BCLKR BDR	BČLKR	
	BDR BDR BFSX	BDR BFSX	BULAK BDR BFSX
		BB_CLK_13M	
	BB_CLK_13M REF_FREQ	REF_FREQ	BB_CLK_13M REF_FREQ

А

В

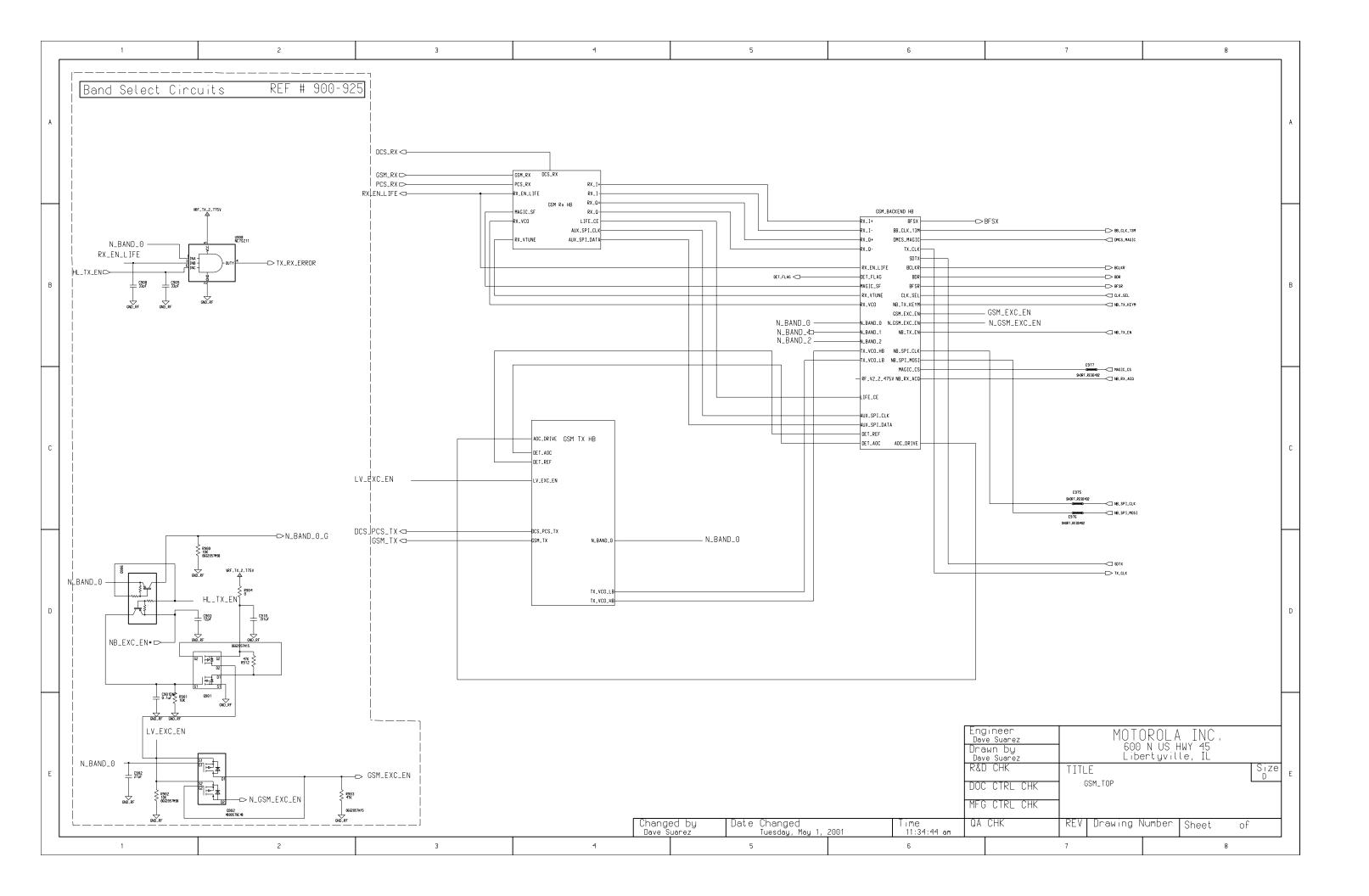
С

D

GND_PWR E6003

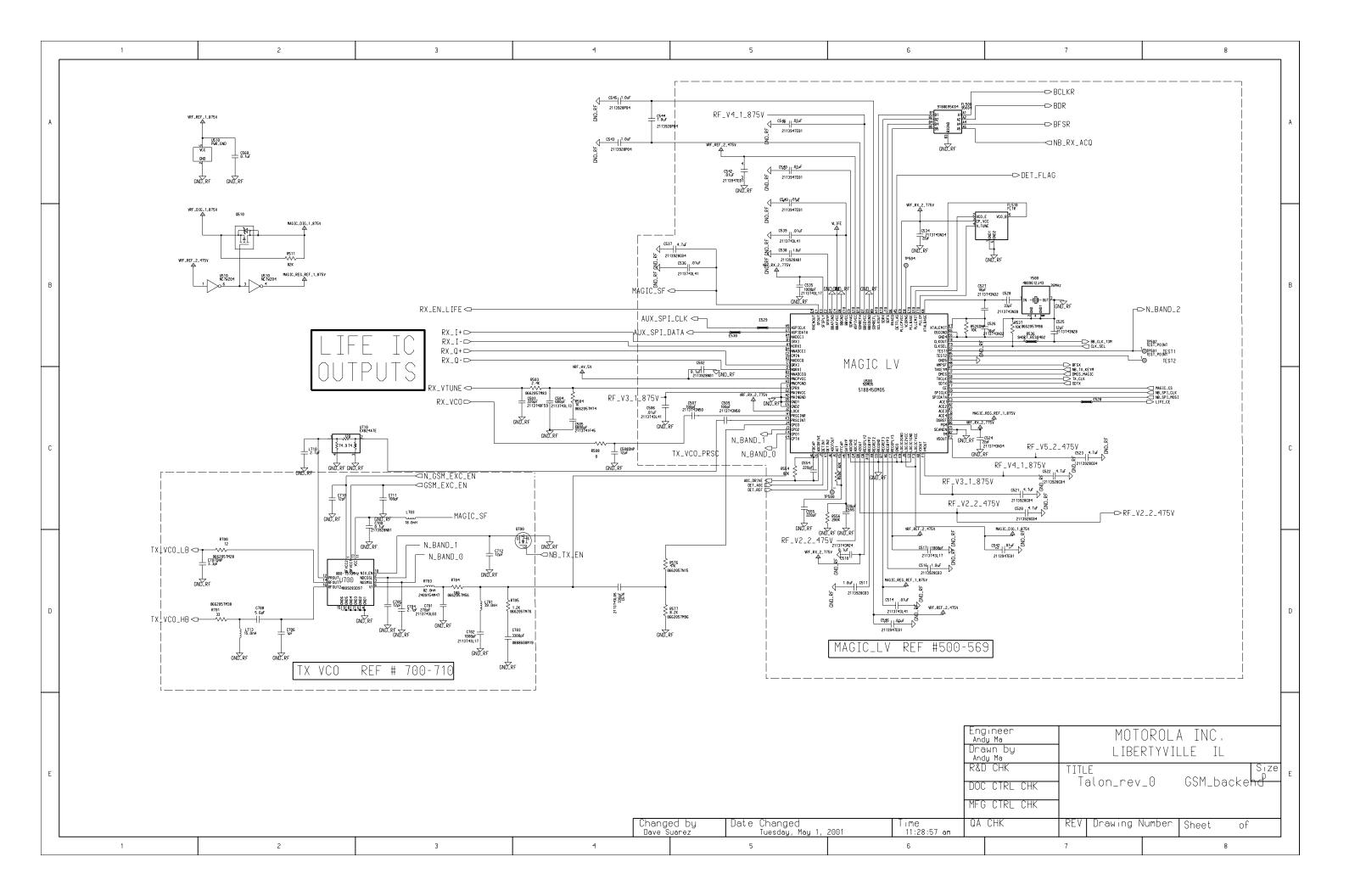
		2	4				
	Engineer: John Kerr	A	A I	MOT	ORC	DLA	INC.
	Drawn by: John Kerr R&D CHK:	TITLE	F .				Size: 11x17
	DOC CTRL CHK:			Paragon			11×17
	MFG CTRL CHK:			Top Level			
	QA CHK:	REV:	Dr	awing N	umber:	Page:	Of:
	Changed by: w14184	۵	Date T	e: hursday,	November	1, 2001	Time: 11:17:40 am
r							

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В		В
c	NBIX_EVD 6, 1, 1, 1, 100 7, 1, 1, 100 8, 2, 2, 01 NBIX_EVND 6, 1, 1, 1, 100 9, 1, 1, 100 9, 1, 1, 100 NBIX_EVND 6, 1, 1, 1, 100 9, 1, 1, 100 9, 1, 1, 100 NBIX_EVND 6, 1, 1, 100 9, 1, 1, 100 9, 1, 1, 100 NBIX_EVND 6, 1, 1, 100 9, 1, 1, 100 9, 1, 1, 100 NBIX_EVND 6, 1, 1, 100 9, 1, 1, 100 9, 1, 1, 100 NBIX_EVND 0, 1, 1, 100 0, 1, 1, 100 0, 10, 11, 100 NBIX_EVND 0, 1, 1, 100 0, 1, 1, 100 0, 1, 1, 100 NBISPL_NOSID 0, 1, 1, 100 0, 1, 1, 100 0, 1, 1, 100 NBISPL_NOSID 0, 1, 1, 100 0, 1, 1, 100 0, 1, 1, 100 NBISPL_NOSID 0, 1, 1, 100 0, 1, 1, 100 0, 1, 1, 100 NBISPL_NOSID 0, 1, 1, 100 0, 1, 1, 100 0, 1, 1, 100 NBISPL_NOSID 0, 1, 1, 100 0, 1, 1, 100 0, 1, 1, 100 NBISPL_NOSID 0, 1, 1, 1, 100 0, 1, 1, 100 0, 1, 1, 100 NBISPL_NOSID 0, 1, 1, 1, 100 0, 1, 1, 1, 100 0, 1, 1, 100 NBISPL_NOSID	С
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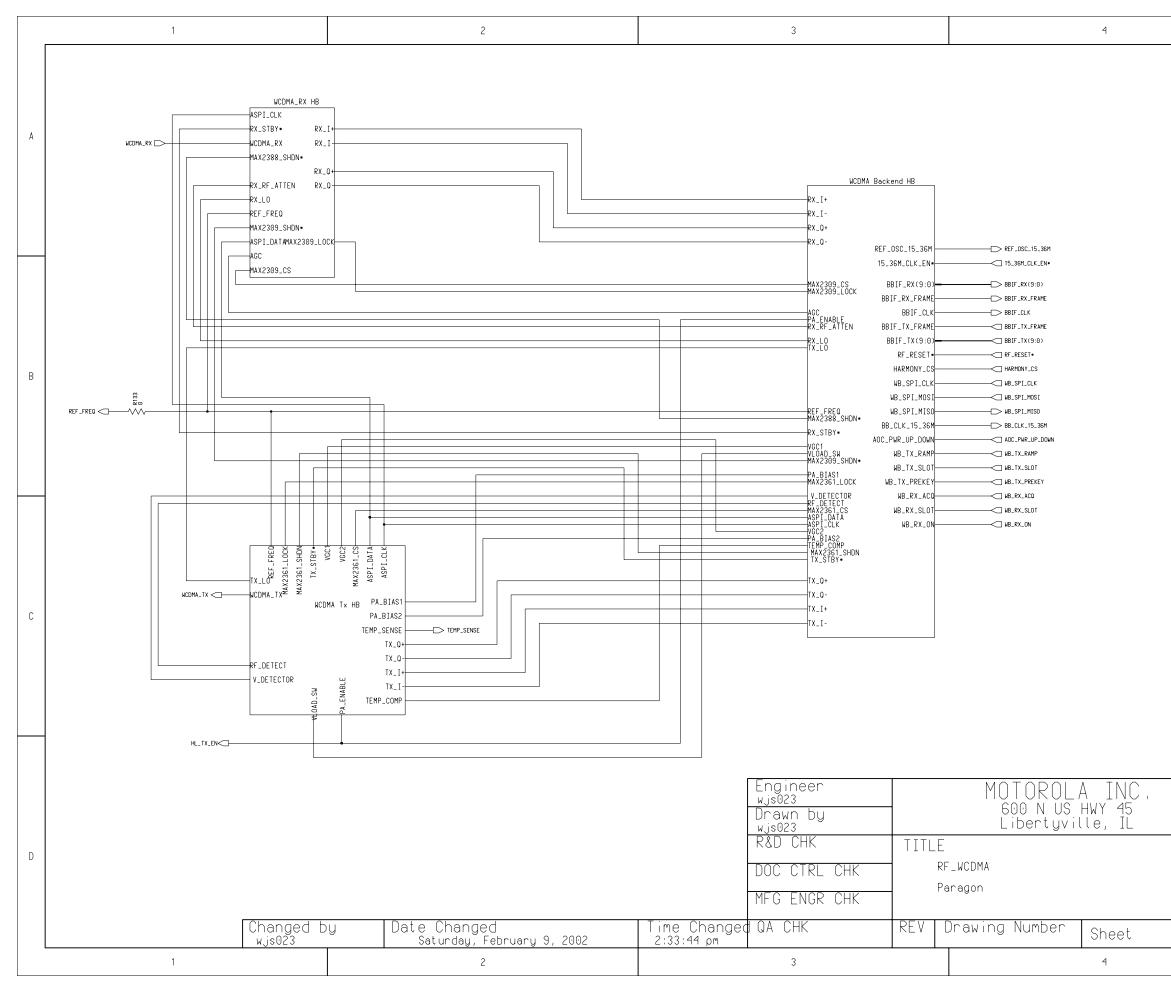


	1	2	3	4	5	6	
A					VRF_REF_2.415V VRF_RX_2.175V Route	2.475V, NOT 2.775V	
В				2113743N38 	GND_RF 2113743N12 0	V.JFE (5502) (5502 (5502) (5502 (5502) () () () () () () () () () (
С			DCS DCS.RX DOD.RF	CARD_EF CCT0 21132F43N28 CARD_EF CCCT0 21132F43N28 CARD_EF CCCT0 21132F43N28 CARD_EF CCCT0 21132F43N28	AUX. SPI_LCE PROJECT ACTION OF A CONTRACT O	C600 213743065 C606 213743065 C605	
D		RX FRONT END FILTE	ERS REF # 650-699				
E	1	2	3	Char Dave	nged by Date Changed Suarez Tuesday, May 1,	DOI	ngineer nan Elfers awn by nan Elfers DCHK DCCTRLC GCTRLC ACHK

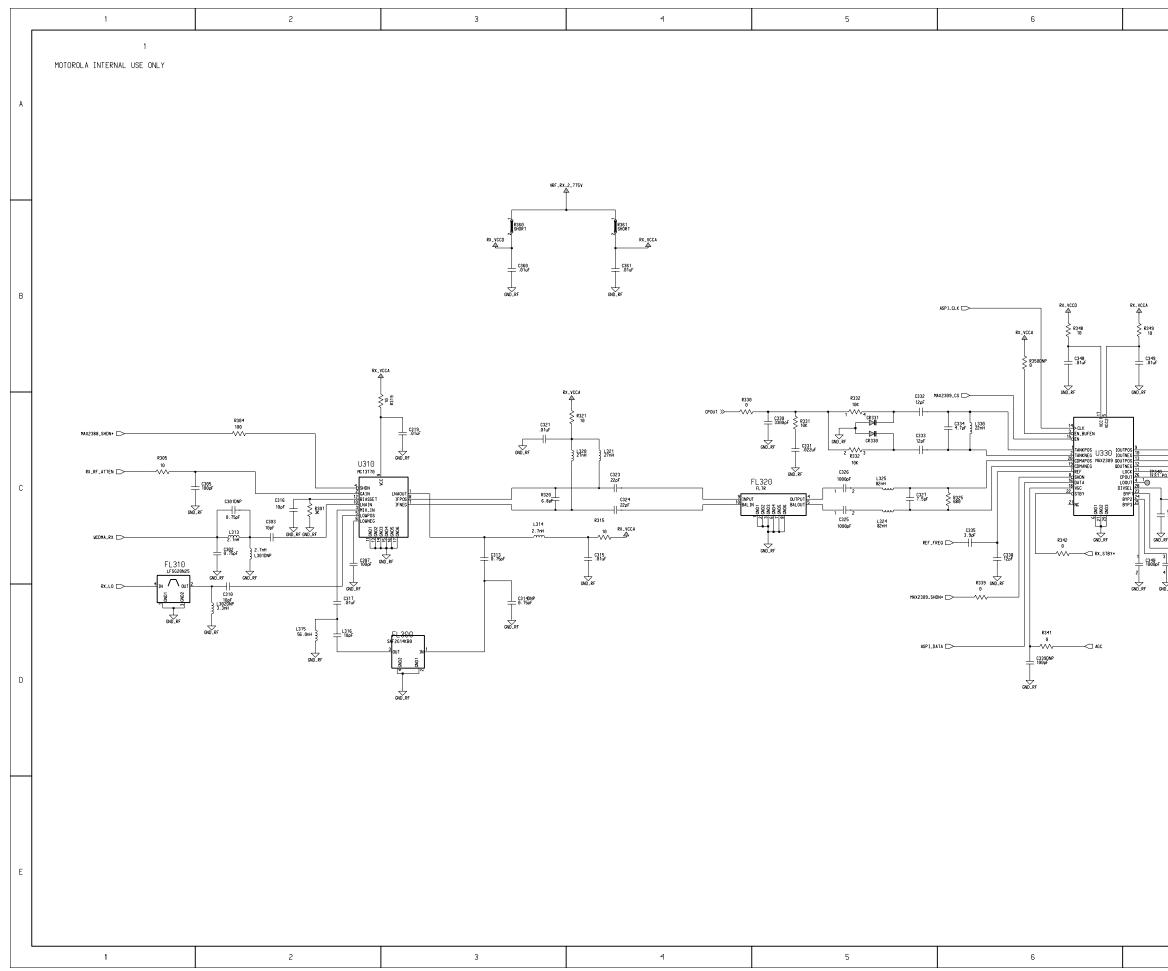
Elfers THOTOKOLA INC i D by 600 N US HWY 45 Elfers Libertyville, IL CHK TITLE CTRL CHK GSM_Rx		7		8	
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eer MOTOROLA INC, <u>Elfers 600 N US HWY 45</u> <u>Elfers Libertyville, IL</u> CHK TITLE Size E CTRL CHK GSM_Rx					С
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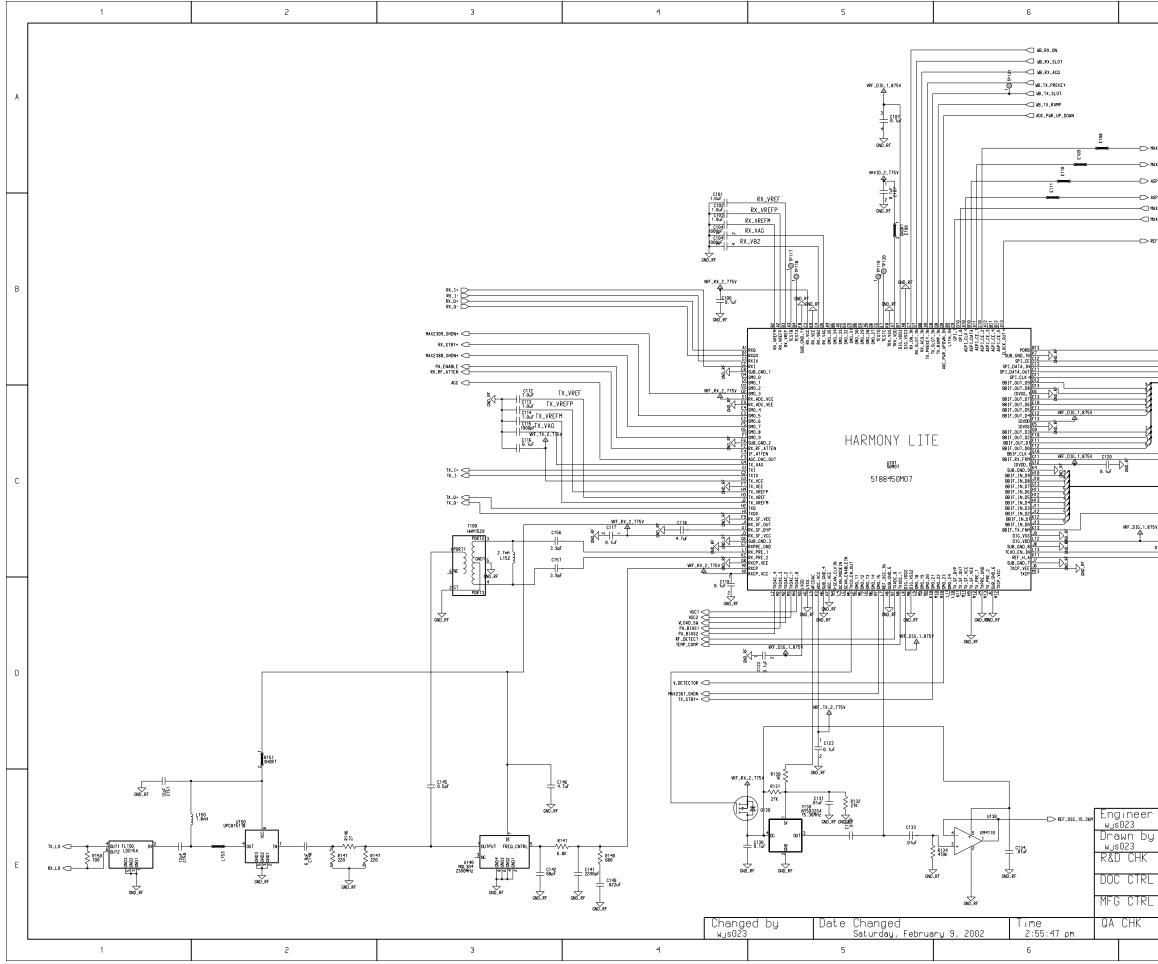
A	1	2	3	4	5	6		7	8 A
В			PA.BATT-		GSM Tr	ansmitter			В
				C804 C804 0 1ur 0 1u					
C		TX_VC0_HB TX_VC0_LB N_BAND_0 AOC_DRIVE	Brcs. N Gos. N Gos. P Zeor	Image: Set of the set	8813 4.1701		3 0662057001 74 10 74 020 14 000 14 000 1000 1	GSM T× Out GSM_Tx	C
D					LV_EXC_EN D	E DET_REF			D
E							Engineer Paul Manente Drawn by Paul Manente R&D CHK	MOT 600 Lit TITLE csm_tx_d	OROLA INC, D N US HWY 45 Dertyville, IL Size D
	1	2	3	Change Dave Su 4	d by Date Changed arez Tuesday, May 1, 5	2001 Time 6	DOC CTRL CHK MFG CTRL CHK QA CHK	GSM_IX_U GSM_Transmit REV Drawing 7	



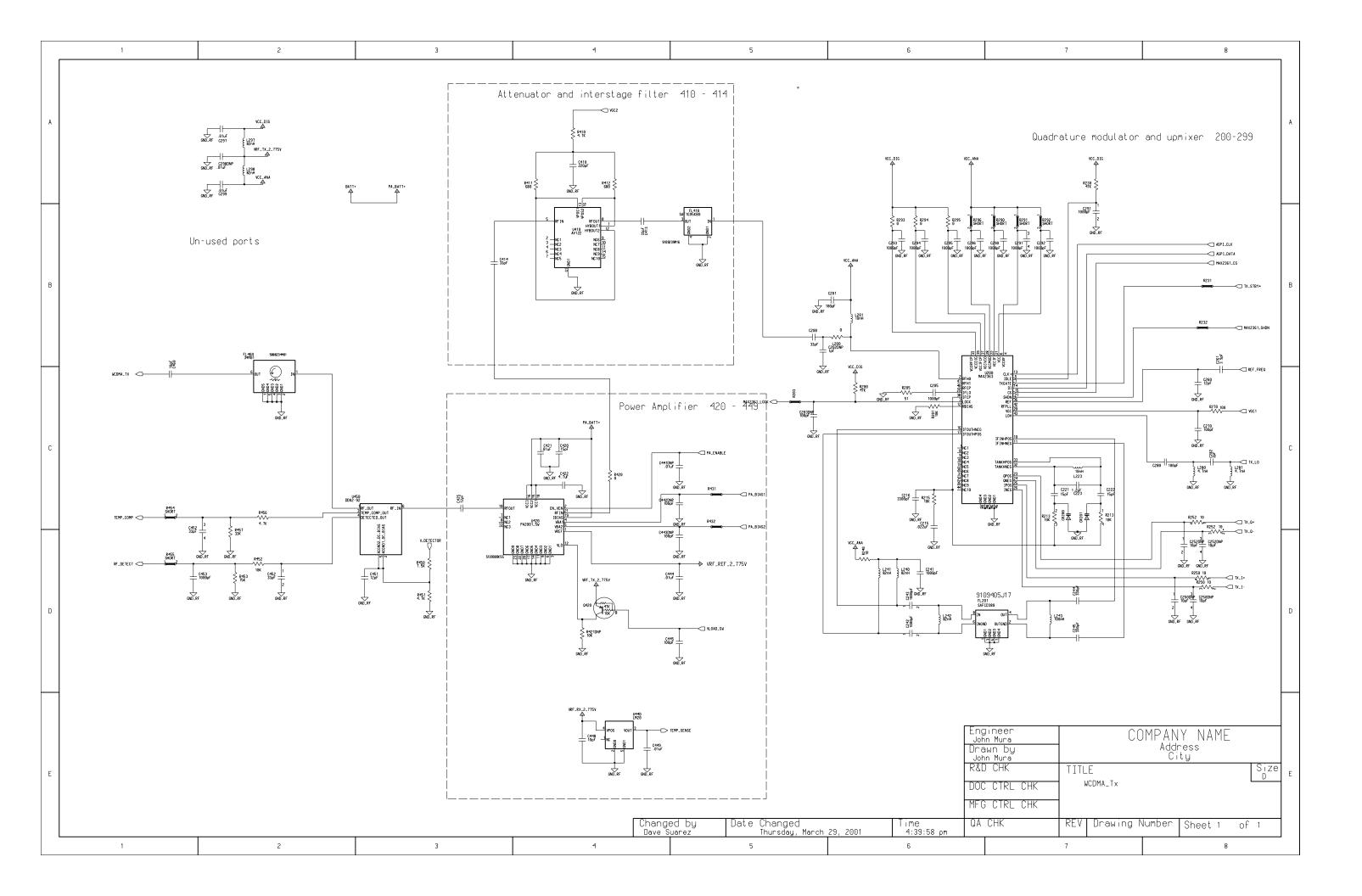


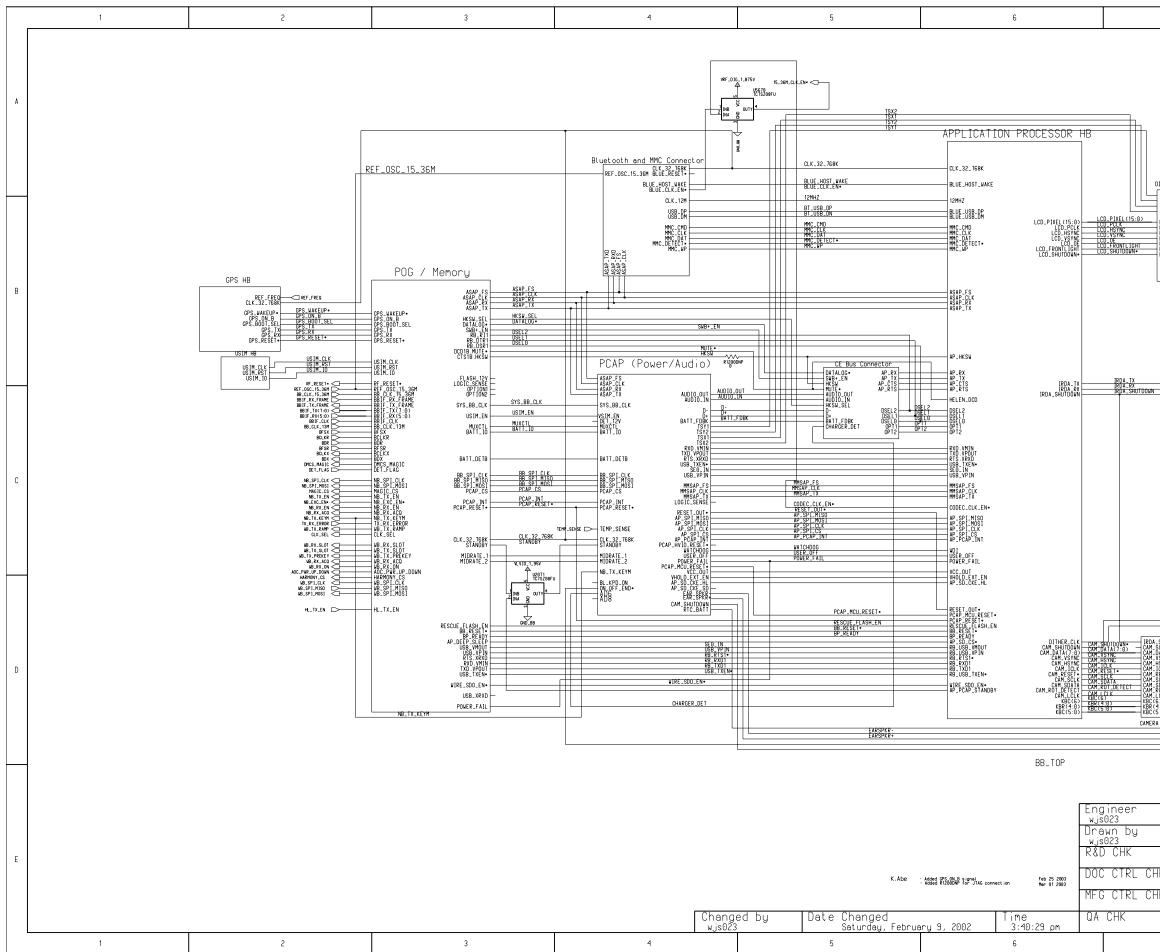


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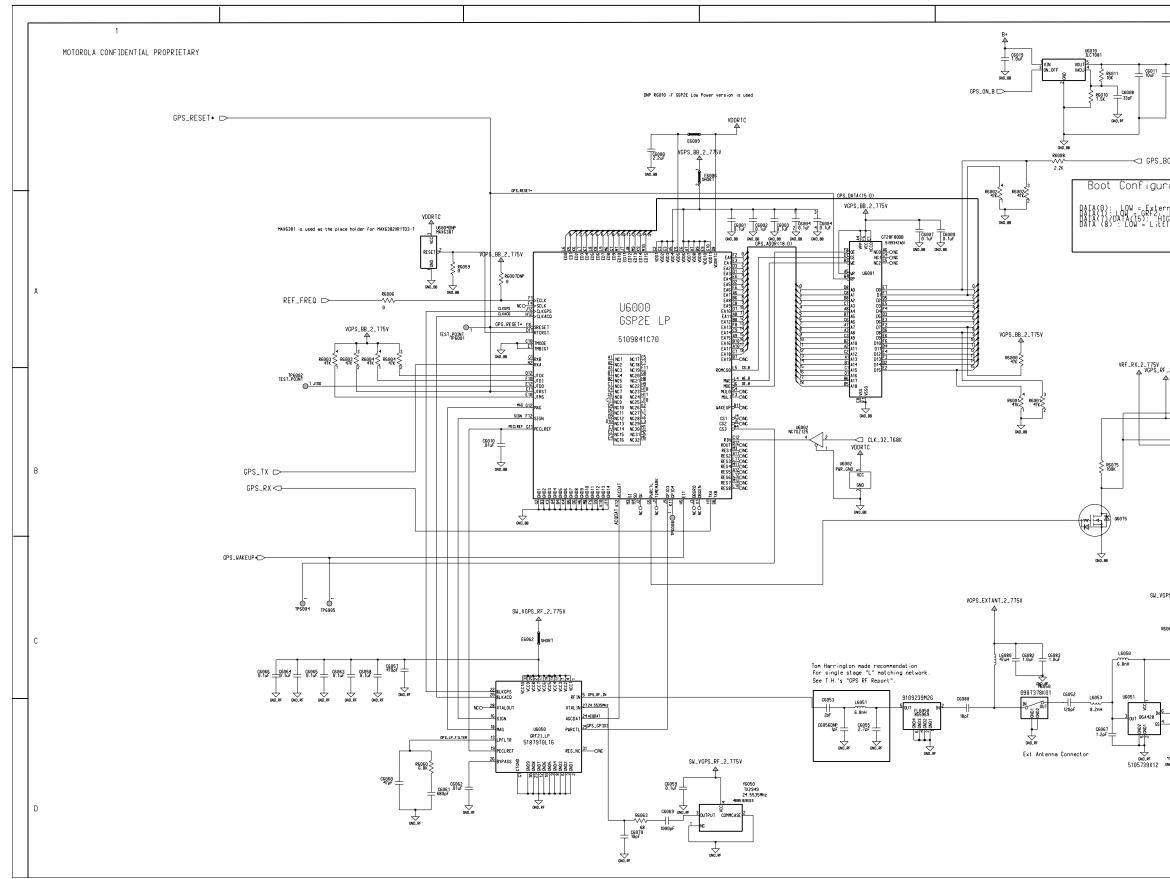


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K2361_CS K2389_CS P1_DATA				A
>].CLK K2363.LOCK ₹2661.LOCK ₹.FRE0		■ FF.££5£1• ■ MARMYY_C5 ■ MARM		В
		BRIF_CLX BRIF_CX_FRAME BRIF_TX<9.0) BRIF_TX.FRAME IS_35H_CLK_EN+ BRIF_TX.FRAME BRIF_TX.FRAME		C
		0		D
СНК	MOT GOU Lit TITLE WCDMA_Backend Paragon REV Drawing 7		' Size D of	E

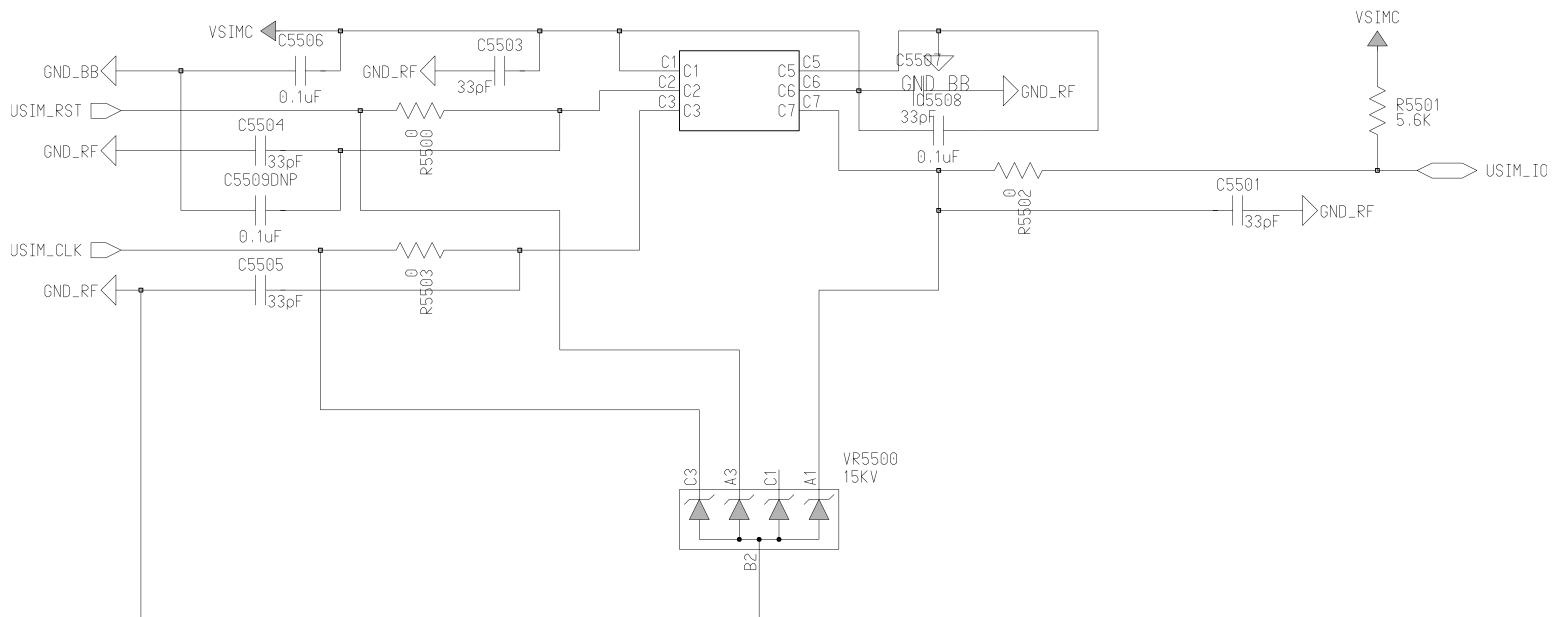




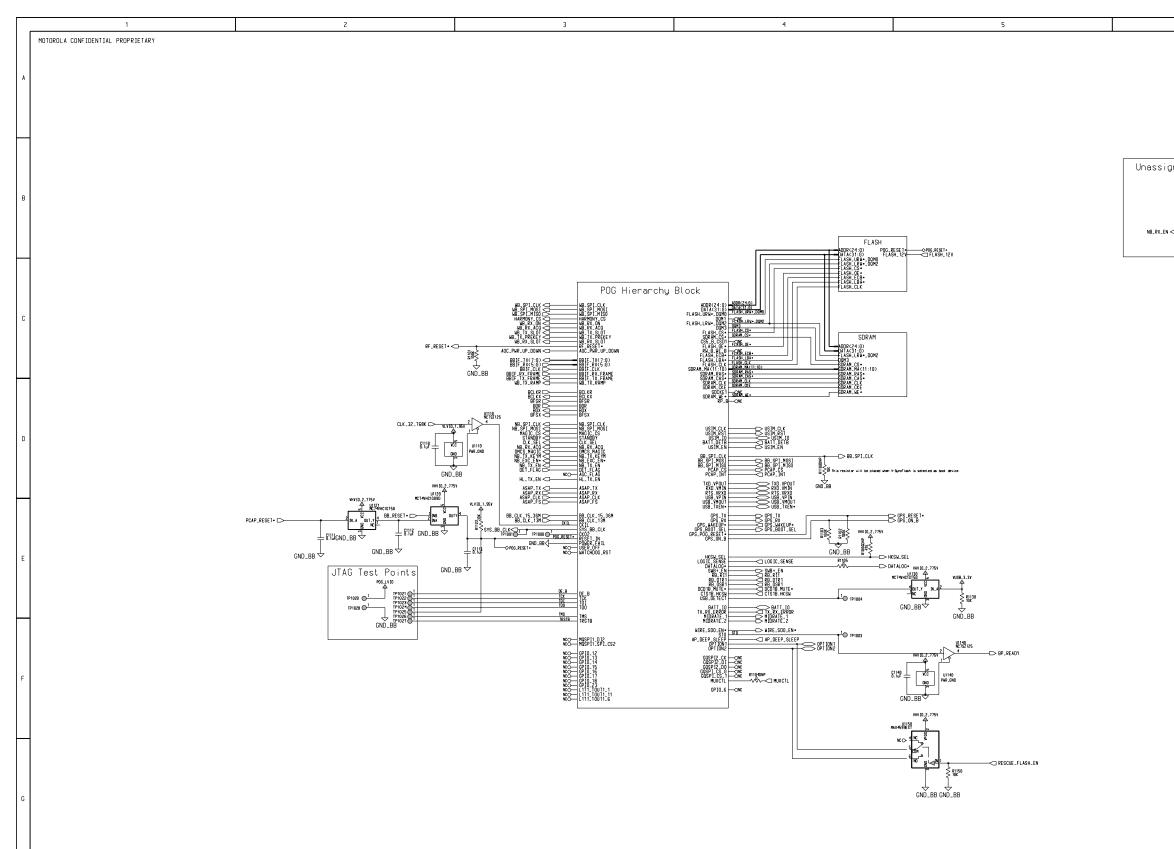
7	8	
DISPLAY CONNECTOR HB		A
Т9Y1 15Y2 15X2 15X2 LCD. P(XEL (15:0) LCD. PCLK LCD. HSYNC LCD. SPNC LCD. SPNC LCD. SHUTDOWN+		В
RGJ.TL RGJ.ER RGJ.SoldSwi		С
A SHUTDOWN LSUSPEND LDATAT'RO VSYMC FORV LEEET SCLE SOLTACTON LOTACTON LOTACTON LOTACTON SCLE SOLTACTON LOTACTON		D
TITLE BB_TOP HK Paragon	DTOROLA INC, 600 N US HWY 45 Libertyville, IL Size Jumber Sheet of 8	E



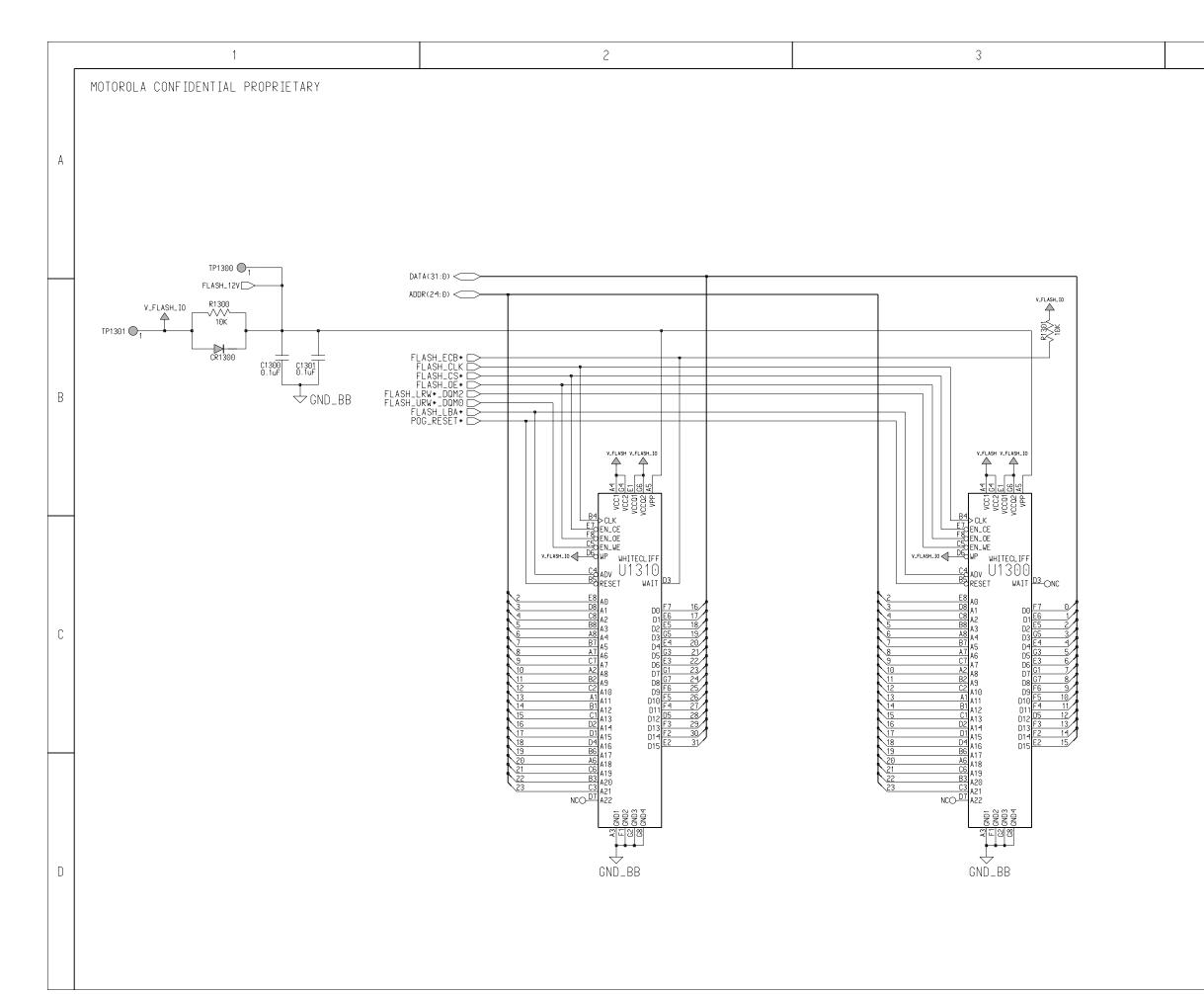
4134	Engineer: Suwinto Gunawan Drawn Du: Suwinto Gunawan	6	MOTOROLA	inc
	KAU CHK:	TITLE :	Paragon GPS Block	Size: 11x24
VGPS_88_2_775V	DOC CTRL CHK: MFG CTRL CHK:			
	QA CHK:		rawing Number: Page:	OF:
	Changed by: Suwinto Gunawan	Dat Wed, Nov	e: 21 2001	Time: 5:03:26 pm
	K.Abe	- Added (GPS_ON_B signal and circuitry rol power source for GPS.	Feb 25 2003
		to con		
0N0_16F				
BOOT_SEL				
ration Settings				
rnal Boot, High = Boot from UART A into DRAM HIGH = Unsuborted IGH/IOW = 16-pit EXI BUS Ite Endian(Intel/ARM), HIGH = Big Endian (SPS)				
IGH/LUW = JG-Dit EXI BUS tte Endian(Intel/ARM), HIGH = Big Endian (SPS)				
F_2_775V				
06659 SW_VCPS_RF_2_775V				
SIGN HE VGPS_EXTANT_2_775V				
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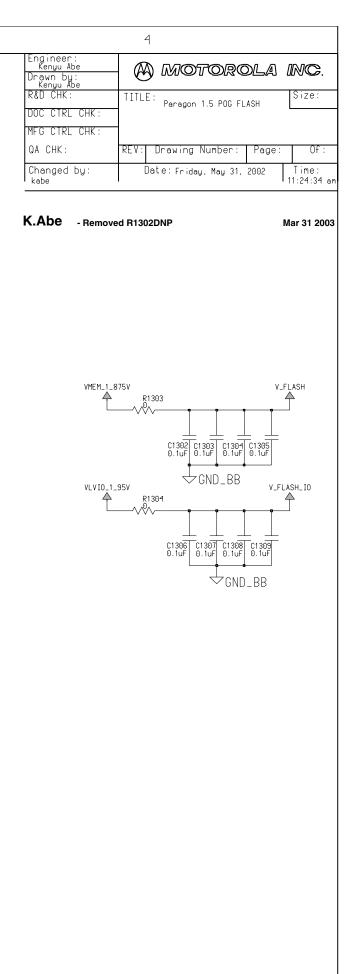


J5500



	torola i
	1.5 POG Main
	Number: Page:
	au. Mau 31, 2002 1
	b to £85 Se
	ETECT signal Oc Lace holder For
	incuitru Oc .8 signal to GPIO.9 Ja
K.AI K.AI	B signal to GPID.9 Ja ler for PCAP_RESET+ Fa
ĸ	on. to POG_LVEO Fi resistor to GPS_ON_E.
ĸ	resistor to GPS_ON_E. R1006DNP M
K.Ab	nical ion
,	E.SYNC signel A
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	1	2	3
	MOTOROLA CONFIDENTIAL PROPRIETARY		
A			VMEM_1_875V V_SDRAM
		64Mbit configuration	VLVIO_1_95V VLVIO_1_95V VLVIO_1_95V VLVIO_1_95V VLVIO_1_95V VLVIO_1_95V VLVIO_1_95V VLVIO_1_95V VLVIO_1_95V VLVIO_1_95V
В		SDRAM_CLK F2 CLK C	C1402 0.10F 0.10F GND_BB
C			_LRW★_DOM2

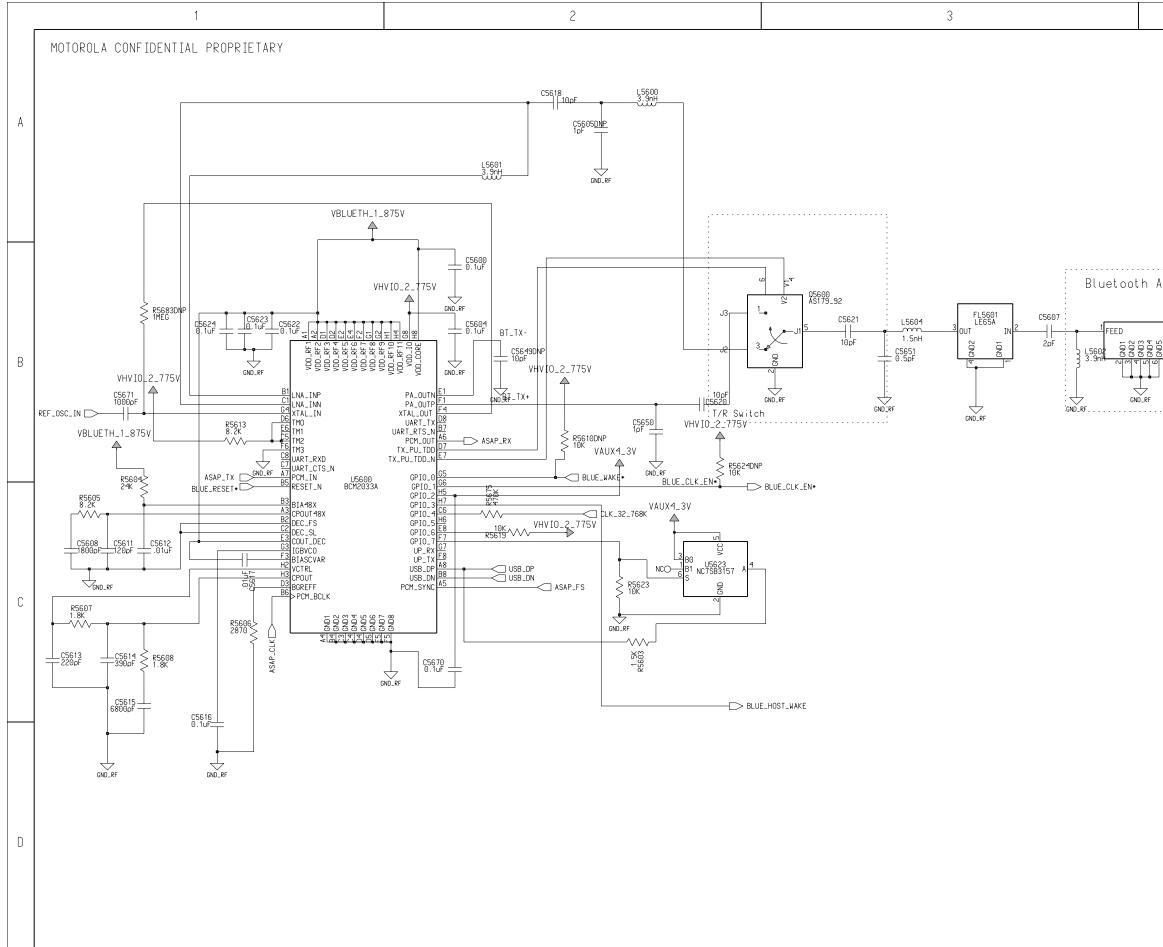
SDRAM size	IAM=0(Non-Interleaved)		
BA Connection	4Mx16	8Mx16	
BAO	SDBA0(ADDR11)	SDBA1(ADDR12)	
BA1	SDBA1(ADDR12)	SDBA2(ADDR13)	

D

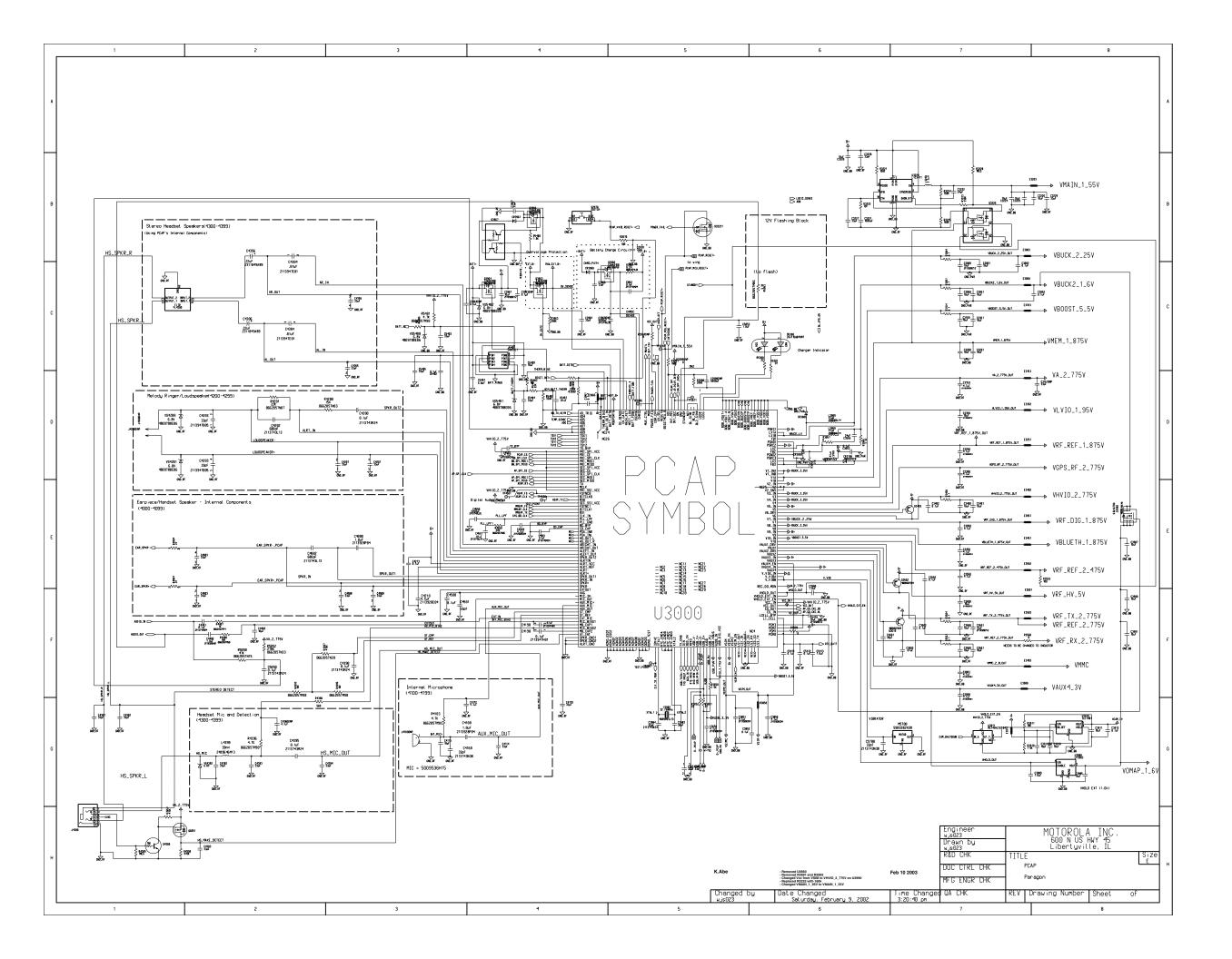
Engineer: Image: Image: <th></th> <th></th>		
R&D CHK: TITLE: Paragon 1.5 POG SDRAM Size: DOC CTRL CHK: MFG CTRL CHK: Page: OF: QA CHK: REV: Drawing Number: Page: OF: Changed by: Date: Friday, May 31, 2002 Time: 11:24:34 am K.Abe - Removed C1402.C1403, C1406 and C1407 Jan 29 2003 Added jumpers to select SDRAM size K.Abe - Configured SDRAM as 64Mbit Mar 01 2003 K.Abe - Updated SDRAM part to 510950A55 Mar 25 2003		NV.
QA CHK: REV: Drawing Number: Page: Of: Changed by: Date: Friday, May 31, 2002 Time: kabe 11:24:34 and 11:24:34 and K.Abe Removed C1402.C1403, C1406 and C1407 Jan 29 2003 - Added jumpers to select SDRAM size Mar 01 2003 K.Abe - Configured SDRAM as 64Mbit Mar 01 2003 K.Abe - Updated SDRAM part to 510950A55 Mar 25 2003	R&D CHK: TITLE: Paragon 1.5 POG SDRAM	Size:
K.Abe - Removed C1402.C1403, C1406 and C1407 Jan 29 2003 - Added jumpers to select SDRAM size Jan 20 2003 K.Abe - Configured SDRAM as 64Mbit Mar 01 2003 K.Abe - Updated SDRAM part to 510950A55 Mar 25 2003		Of:
- Added jumpers to select SDRAM size K.Abe - Configured SDRAM as 64Mbit Mar 01 2003 K.Abe - Updated SDRAM part to 510950A55 Mar 25 2003	Changed by: Date: Friday, May 31, 2002 11 kabe	
- Added jumpers to select SDRAM size K.Abe - Configured SDRAM as 64Mbit Mar 01 2003 K.Abe - Updated SDRAM part to 510950A55 Mar 25 2003	K Abo	
K.Abe - Updated SDRAM part to 510950A55 Mar 25 2003		an 29 2003
		ar 01 2003

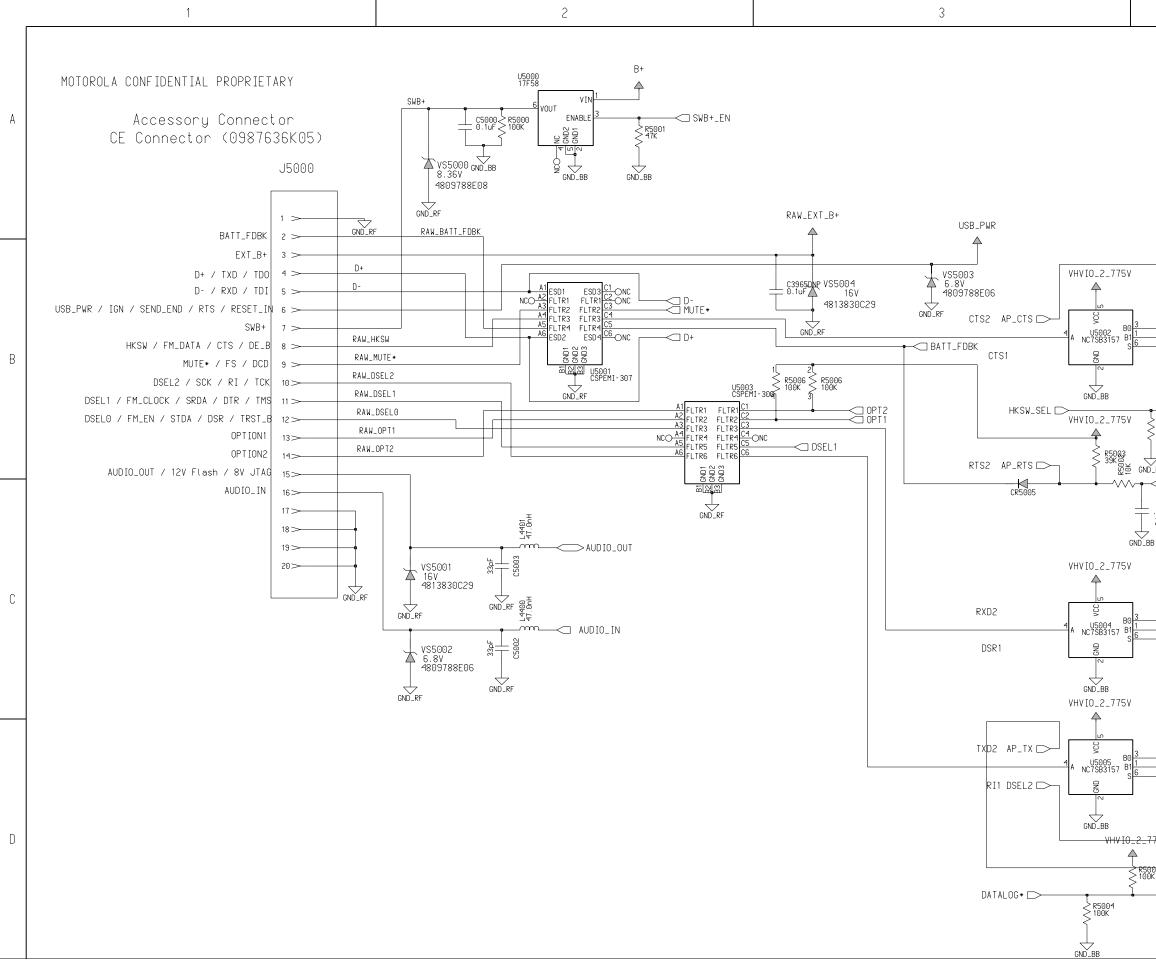
	1	2	3	4
A	MOTOROLA CONFIDENTIAL PROPRIETARY		BLUELOOT H BLUE_CLK_EN+BLUE_RESET+ BLUE_WAKE+ BLUE_HOST_WAKE USB_DN REF_OSC_IN ASAP_FS CLK_32_768K ASAP_CX ASAP_TX	
В				
С		J3900 1 → J3900 3 → J3900 5 → J3900 8 → J3900 10 → J3900 12 → J3900 14 →	MMC_C/LK MMC_C/LK MMC_DAT 100 R3904	399200P VR3912 C3993 VR3914 010F 6.8V 010F 6.8V
D				

4 1 3 A Loginese Karr Dram By Karr DOC CTRL CHK: NFC CTRL CHK: NFC CTRL CHK: OA CHK: Changed bu: John Kerr b: Wednesday, August 8, 2001 1 (17) 1 (5	
R&D CHK: TITLE: TALON Size: 11x21 DOC CIRL CHK: USIM/BT Board Connector WFG CIRL CHK: USIM/BT Board Connector 0A CHK: REV: Drawing Number: Page: Of: Changed bu: Date: Time:	413A	Engineer: John S. Kerr Drawn by:	(A) MOTOROLA	INC.
MFG CIRL CHK: QA CHK: REV: Drawing Number: Page: OF: Changed bu: Date: Time:		R&D CHK:	TALON	Size: 11x21
Changed by: Date: Time:			USIM/BT Board Connector	
Unanged by: Unanged by: Vednesday, August 8, 2001 1:43:54 pr			· · ·	
		Changed by: John Kerr	Date: Wednesday, August 8, 2001	lime: 4:43:54 pm

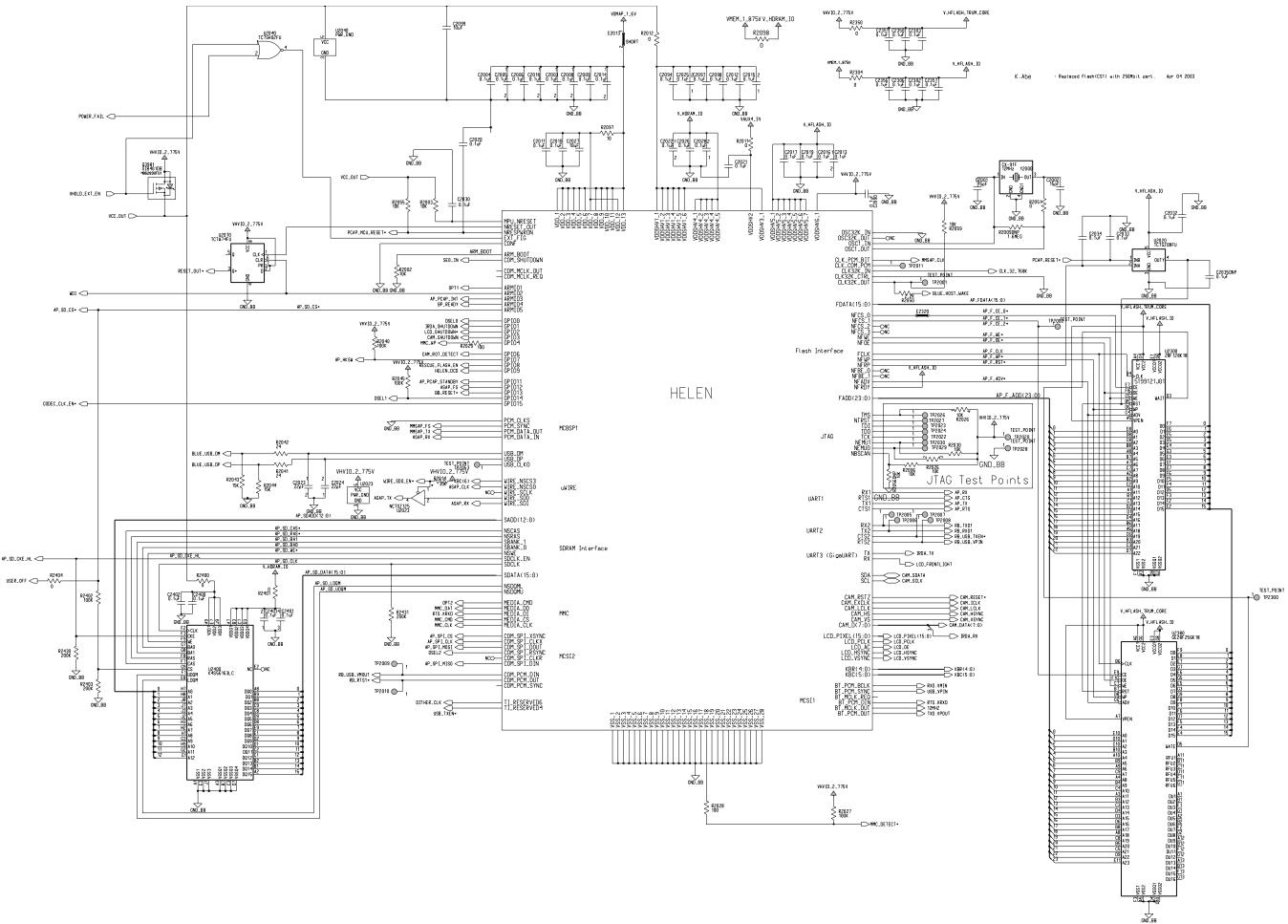


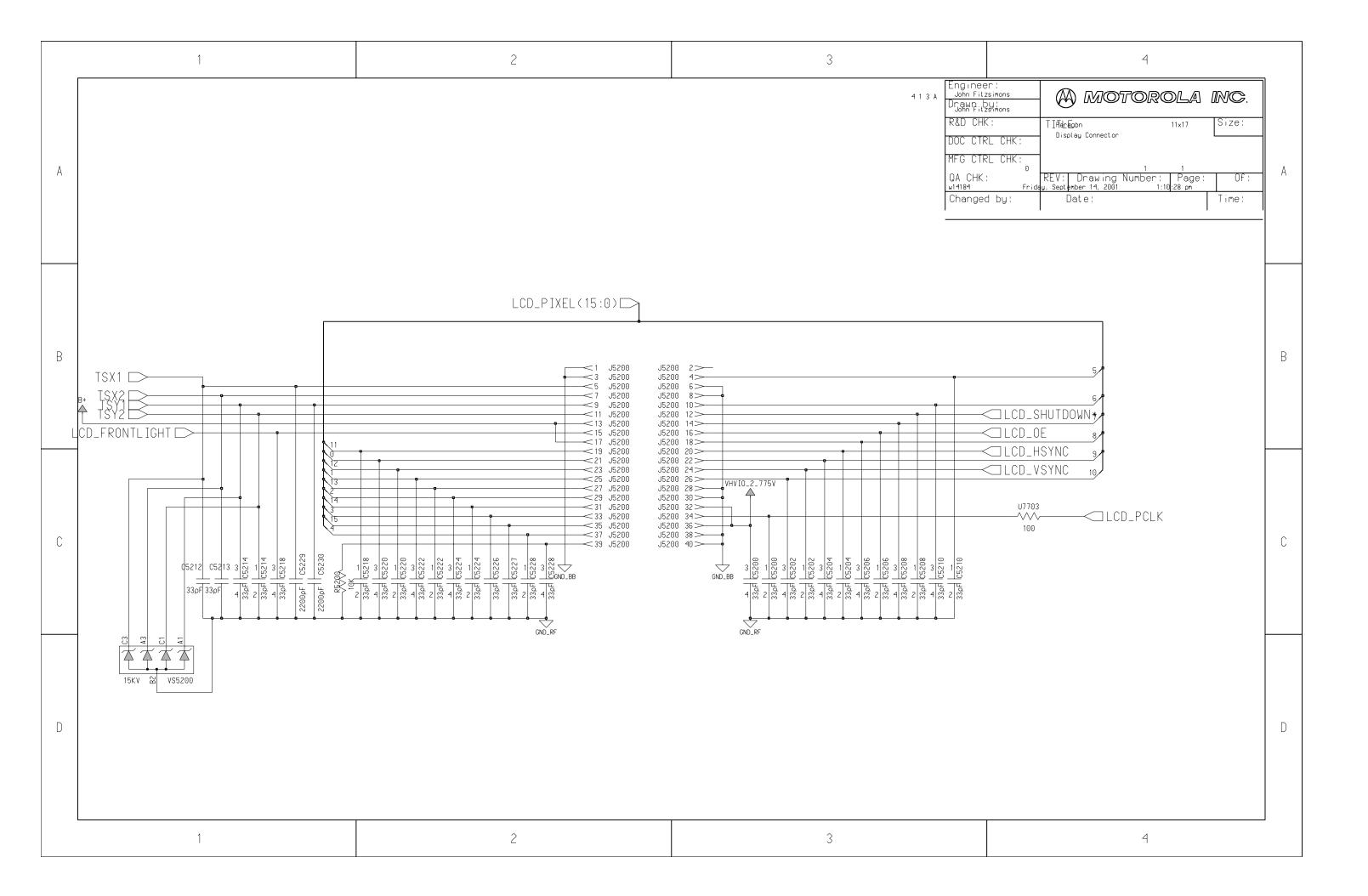
		4		
	Engineer: John Kerr Drawn by:	A	MOTOROLA	
)		
	R&D CHK:	TITLE	: Bluetooth	Size:
	DOC CTRL CHK:		DIUCIOUN	
	MFG CTRL CHK: QA CHK:		Deauing Number	
			Drawing Number: Page:	
	Changed by: John Kerr	Ue	ite: November 1, 2001	Time:
Ant enna				
A5600 YCE-5248				





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	Engineer:		
	Engineer: John Kerr Drawn by:	A MOTOROLA	
	R&D CHK: DOC CTRL CHK:	TITLE: PARAGON	Size:
	MFG CTRL CHK:	CE Accessory Connecto	pr
	QA CHK:	REV: Drawing Number: Page:	Of:
	Changed by:	Date: 11/01/01	Time:
	John S. Kerr	Changed bypass ground on Audio	
	11/01/01	IN/OUT from BB to GND_RF.	
	1		
— HKSI			
₹			
5 "/"			
⊥ D_BB			
	ER_DET		
C5010 .01uF			
2113743L	41		
BB			
2	AP_RX DSEL0		
	50220		
775V			
007DNP			





	1	2	3	
А	MOTOROLA INTERNAL USE ONLY			
В		C6510DNP 330F 1.00F S	VHV I0_2_775V VHV I0_2_775V HSDL 3202 5162852A33 SD SD SD SD SD SD SD SD SD SD	
С		C6502 330F GND_RF	TXD B IRDA RX GS G C Sold GND_RF	
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Engineer:	
Drawn by:	A MOTOROLA INC.
R&D CHK:	TITLE: Size:
DOC CTRL CHK:	1
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QA CHK:	REV: Drawing Number: Page: Of:
Changed by:	Date: Time:

